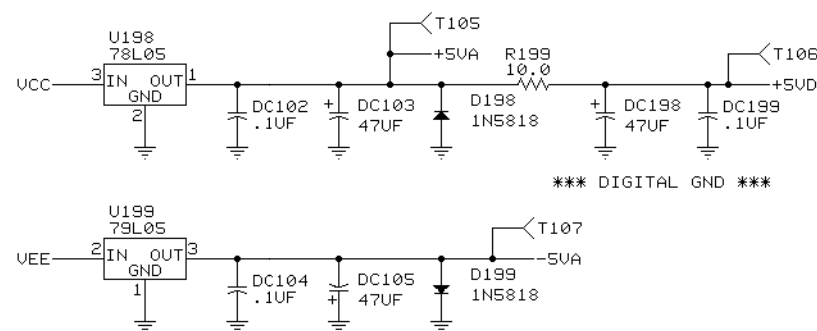
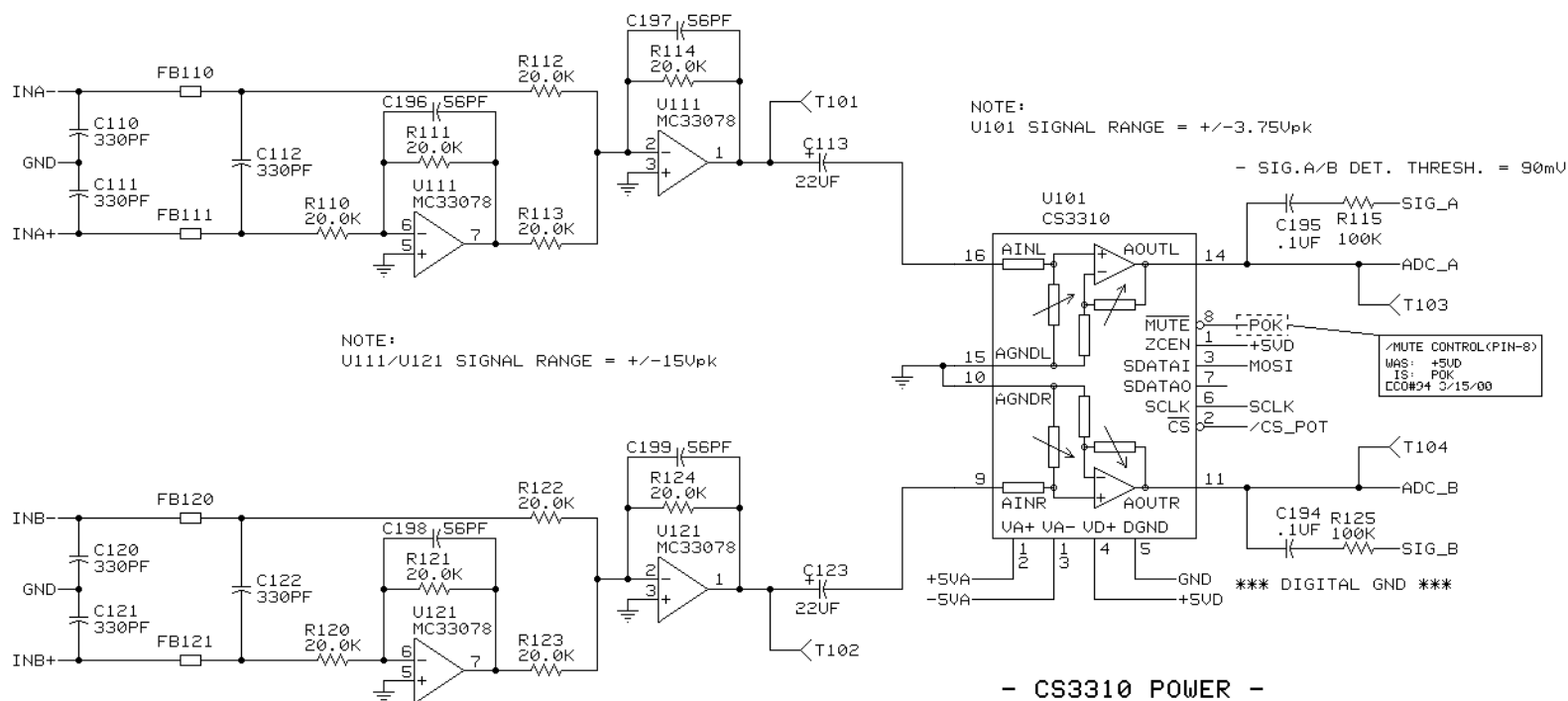
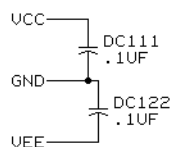




FOR U111 / U121



CH.A/B INPUTS / LEVEL ADJUST

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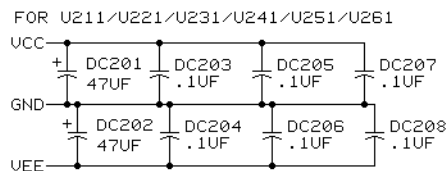
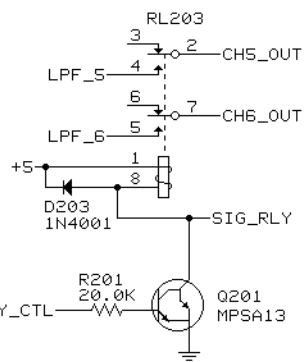
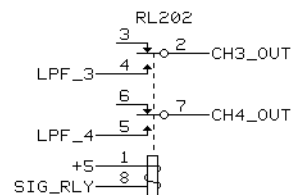
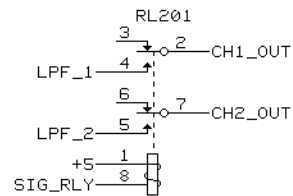
DSP PEQ (MAIN)

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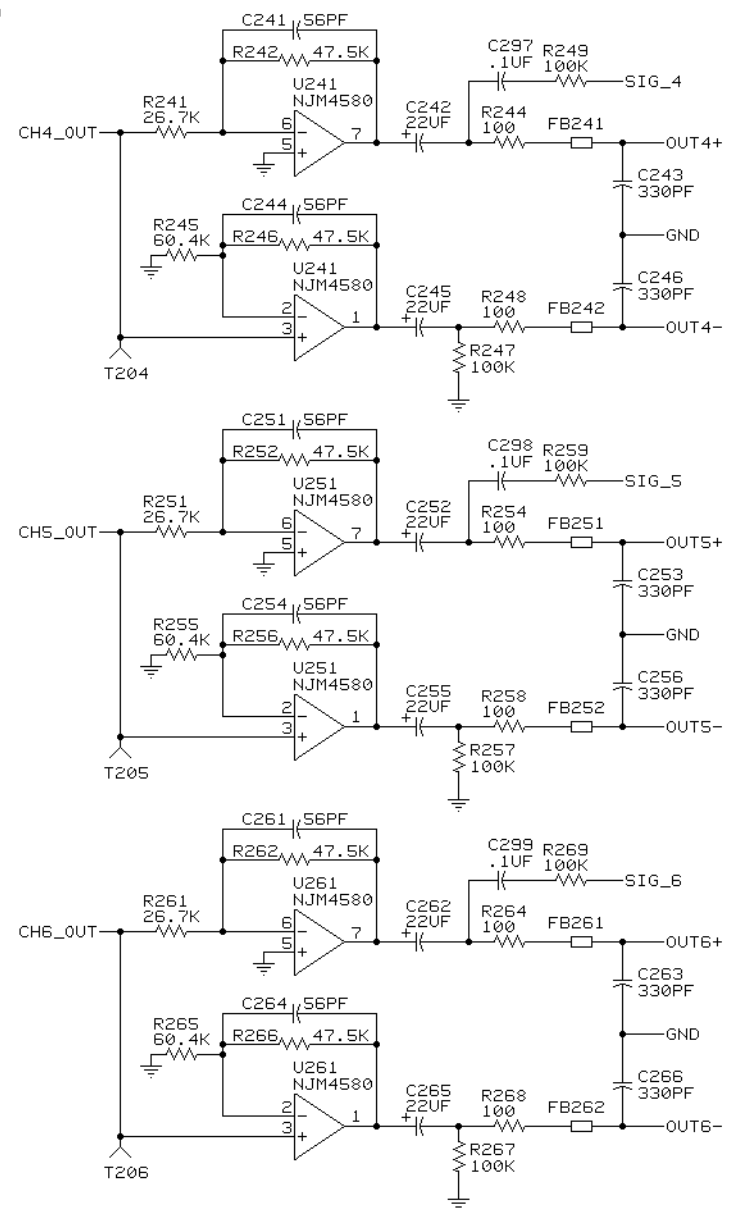
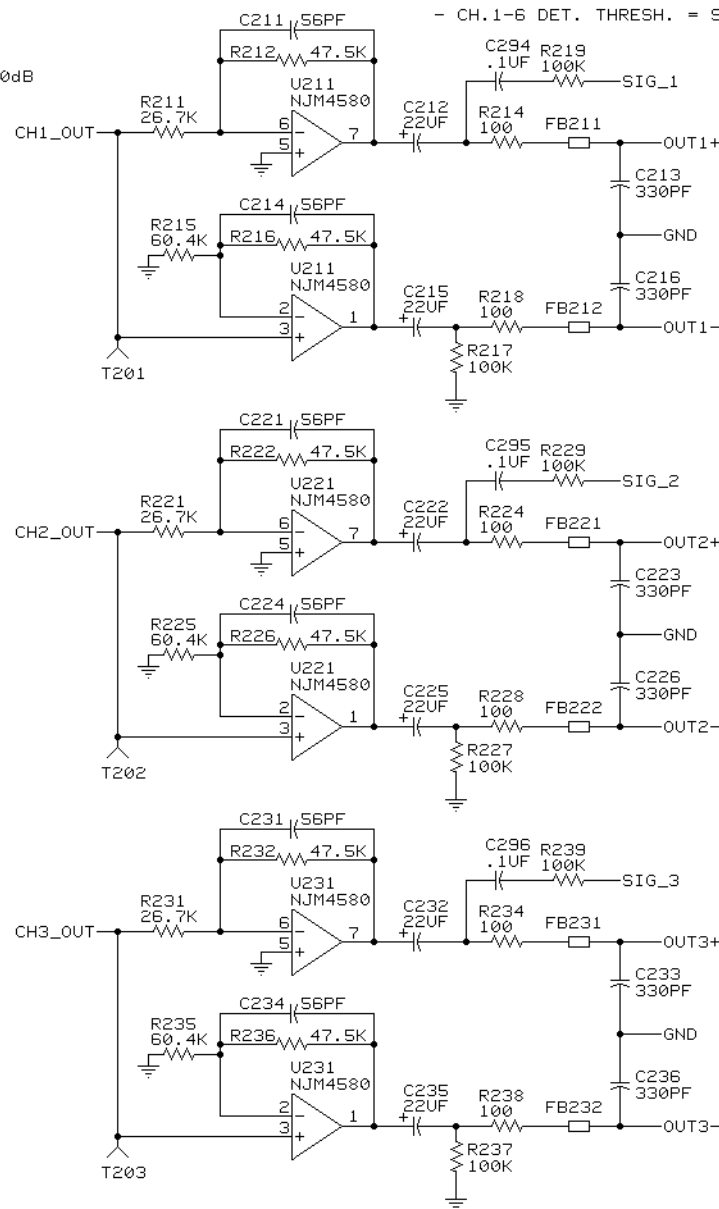
NOTE:

- CH.1-6 GAINSTAGE = 5.0dB
- UNBALANCED TO BALANCED = 6.0dB
- TOTAL GAIN = 11.0dB
- SIGNAL RANGE = +/-150 μ V

- CH.1-6 DET. THRESH. = 90mV



CHANNELS 1-6 BALANCED OUTPUT DRIVERS

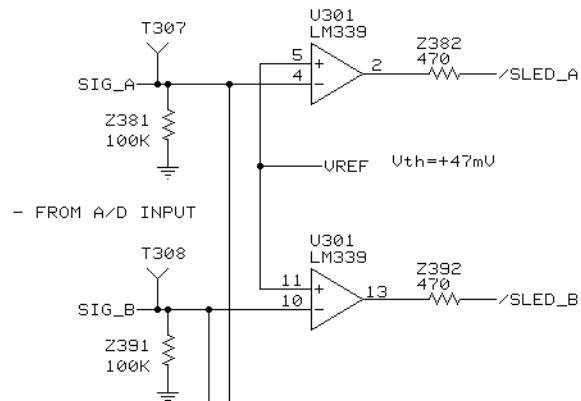


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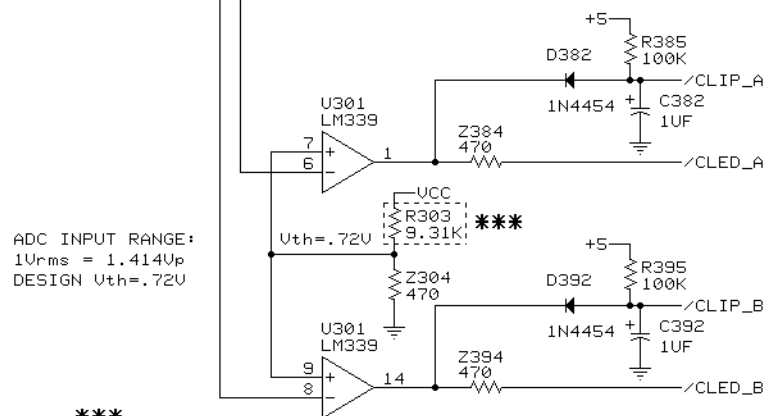
DSP PEQ (MAIN)

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A/D SIGNAL PRESENCE



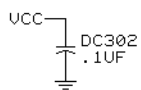
A/D SIGNAL CLIPPING



ADC INPUT RANGE:
1Vrms = 1.414Up
DESIGN Vth=.72V

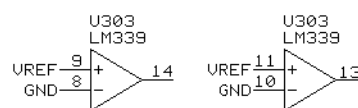
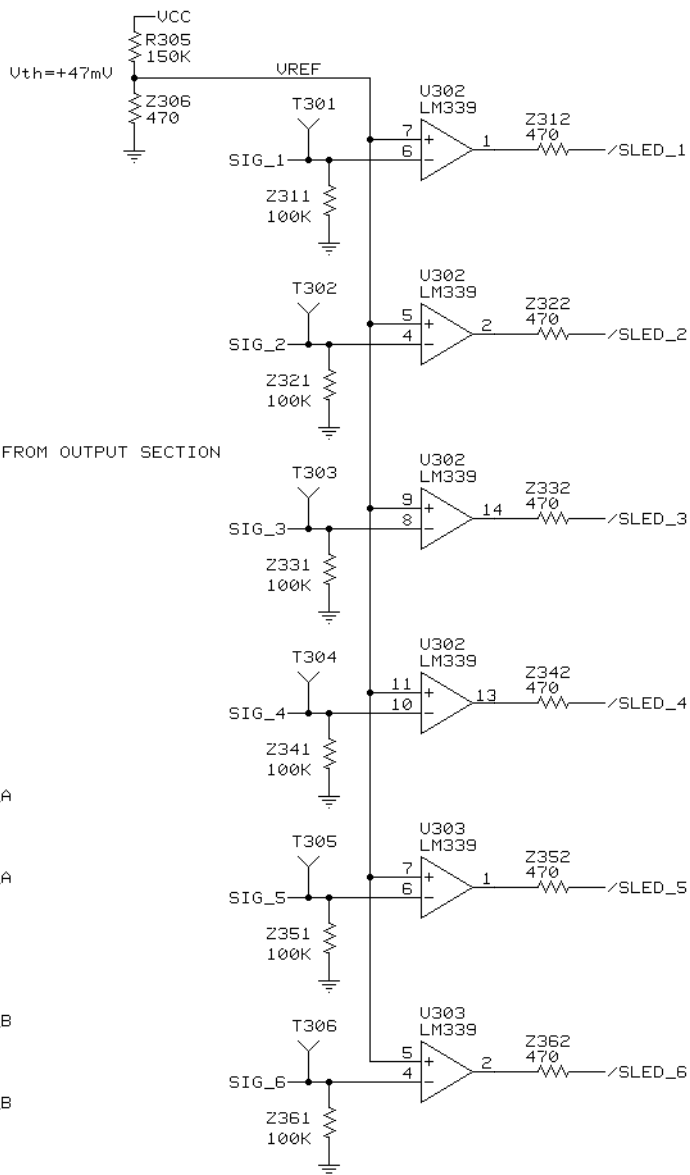
R303(CLIP DET.THRESH.)
WAS: 4.64K (THR=1.38V)
IS: 9.31K (THR=0.72V)
CC0#24 3/15/00

FOR U301/U302/U303



CHANNEL LEVEL DETECTORS

OUTPUT SIGNAL PRESENCE



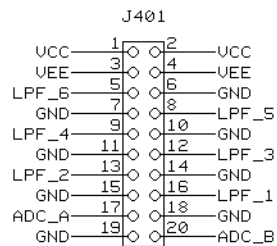
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DSP PEQ (MAIN)

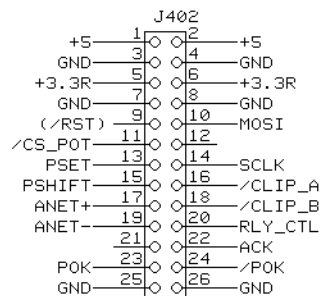
D	626M.P03	002-1810	REV B1
DATE:	10/02/02 15:36:09	SHEET	03 OF 08

MAIN TO DAUGHTER HEADERS

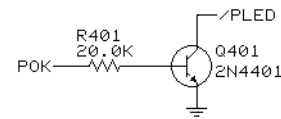
AUDIO/PWR



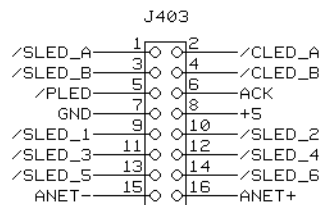
CONTROL/PWR



PWR OK LED DRIVER



MAIN TO FRONT PANEL HEADER

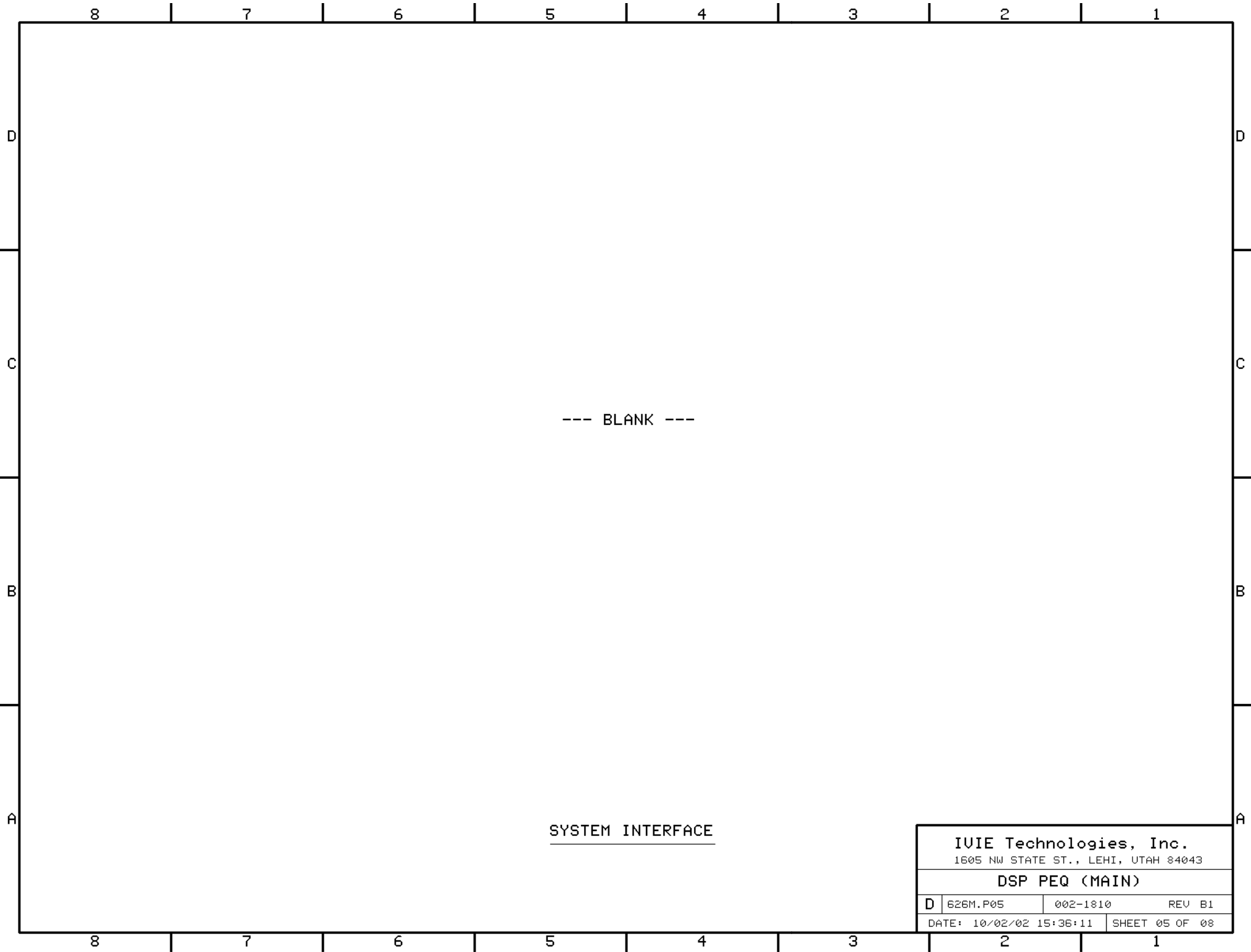


SYSTEM INTERFACE

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DSP PEQ (MAIN)

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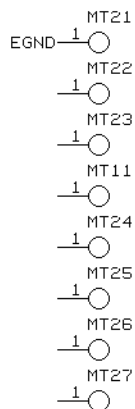


SYSTEM INTERFACE

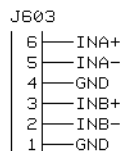
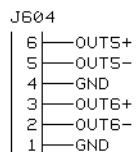
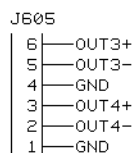
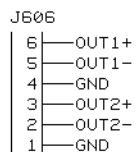
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DSP PEQ (MAIN)			
D	626M.P05	002-1810	REV B1
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REAR PANEL CONNECTORS

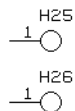
MECHANICAL SUPPORT



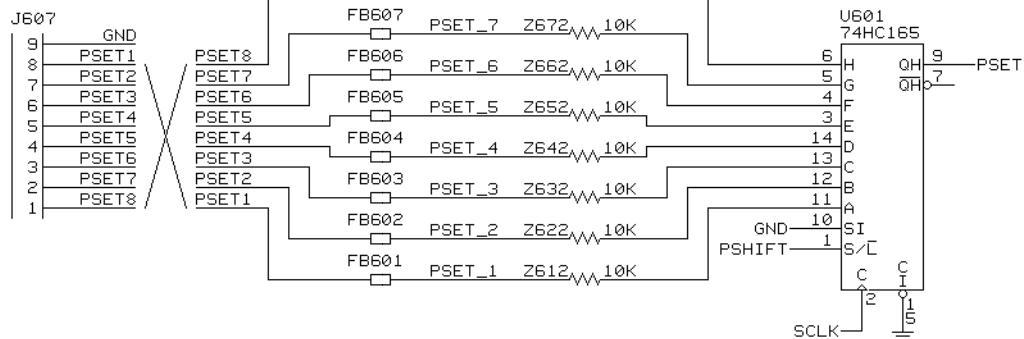
AUDIO CHANNELS



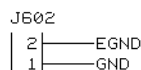
HEATSINK MOUNTS



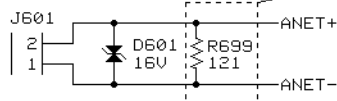
PRESETS 1-8



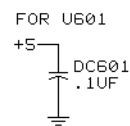
GROUND LINK



AUDIONET (REAR PANEL)



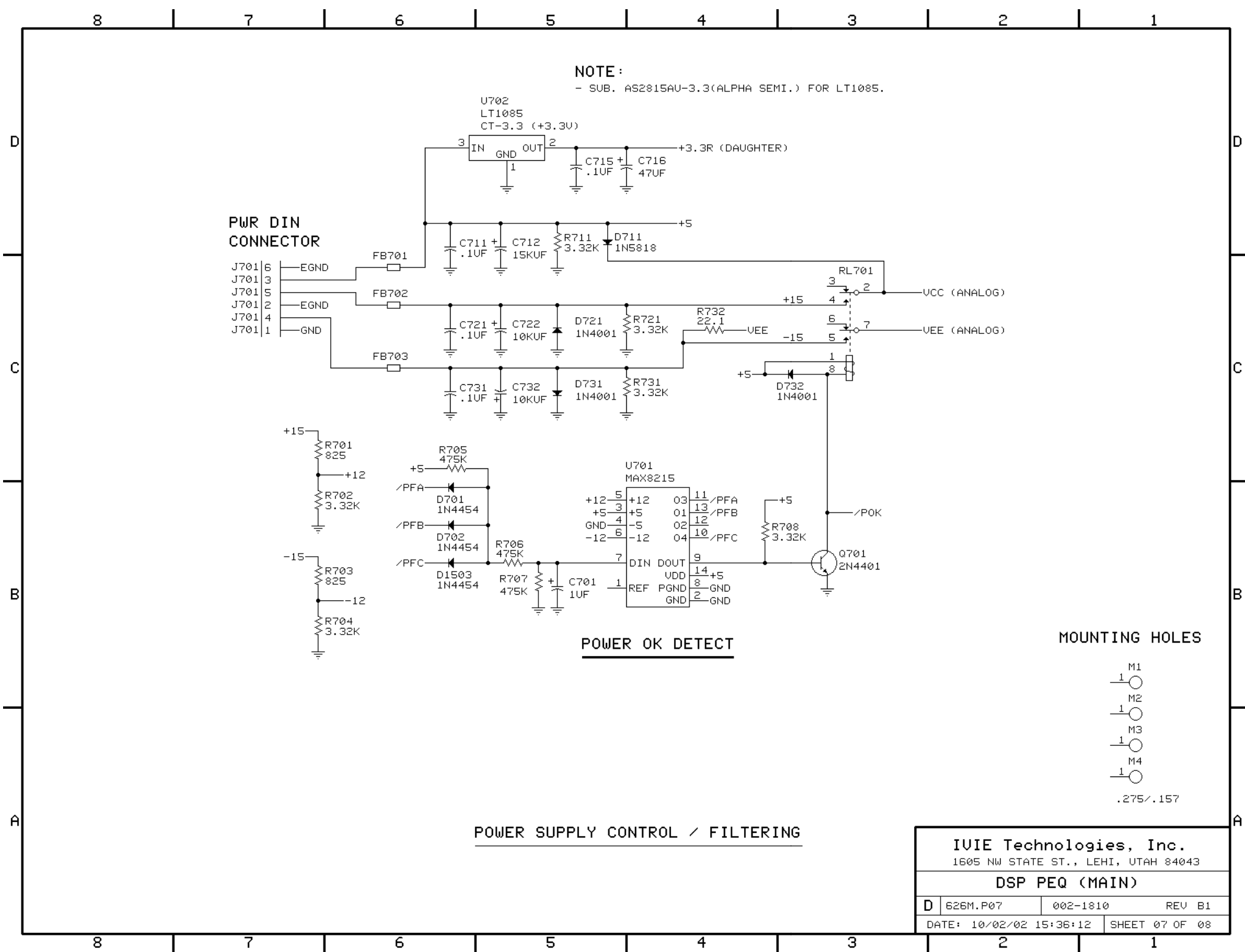
AUDIO/CONTROL CONNECTORS



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DSP PEQ (MAIN)

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DSP PEQ (MAIN)		
D	626M.P07	002-1810 REV B1
DATE: 10/02/02 15:36:12		SHEET 07 OF 08

